

Appl. No. 09/971,097

Amdt. Dated 8/4/2005

Response to Office Action Dated 05/10/2005

Amendments to the Specification:

Please replace the ninth paragraph on page 3 with the amended paragraph:

Fig. 3 is an overall diagram of the switching apparatus where there are a number of source ports 10 numbered 0 through 63 each receiving from, for example a framer which normally puts together a digital data packet, at a rate of 10 Gbps. The source ports 10 include a TM (traffic manager) and a communications processor (CP) and are labeled CP/TM. Each source port has an 8-line output port, each individually coupled to an input port of switch elements SE0 through SE7 which together create a so-called switching fabric. In turn, the eight switching elements each with 64 input ports and 64 output ports are similarly connected on an output side to destination ports 12 also designated CP/TM which have 8-line inputs and are numbered 0 through 63. The combination of the 64 source ports and 64 destination ports make up a 64 port full duplex port.